

# Lab 1 – Part 2: Debugging and Basic Assembly

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- **Addition using Assembly programming.**
- **Bitwise Operations and Masking.**
- **Learn how to debug your program.**
- **Write a small assembly program.**

# Basic Assembly program – main.s



```
.syntax unified
.global main
.include "stm32l476xx_constants.s"
main:
    // Configure clock speed
    // Configure peripherals (GPIO)
    // Your program logic goes here!
stop:    B          stop // dead loop & program hangs here
```

# Load Constant Values into Registers



- You can use R0 to R12 to hold your “variables”.
- **MOV Rd, #<immed\_8>**
  - Loads a 8-bit immediate value (constant) to the register.
  - Example:
    - `MOV R0, #0xFF`
    - R0 is now equal to 255 in decimal.
- **LDR Rd, =<immed\_8> or LDR Rd, =#<immed\_8>**
  - Loads a 8-bit immediate value (constant) to the register.
  - Example:
    - `LDR R0, =0xFF` or `LDR R0, =#0xFF`
    - R0 is now equal to 255 in decimal.
- **LDR Rd, =<immed\_32> or LDR Rd, =#<immed\_32>**
  - Pseudo-instruction. Loads a 32-bit immediate value (constant) to the register.

# Simple Addition in Assembly



- **ADD {Rd,} Rn, Op2**
  - Does NOT update NZCV flags.
- **ADDS {Rd,} Rn, Op2**
  - Updates NZCV flags.

```
.syntax unified
.global main

main:
    MOV R0, #10          // R0 = 10 (decimal)
    MOV R1, #1            // R1 = 1 (decimal)
    ADD R0, R0, R1        // R0 = R0 + R1 = 11
stop:    B               stop // dead loop & program hangs here
```

# Bitwise Operations in Assembly



- R0 = 0xA2; R1 = 0x34;**

## AND

	R0	10100010
	R1	00110100
<b>AND</b>	R2, R0, R1	<b>00100000</b>

## OR

	R0	10100010
	R1	00110100
<b>ORR</b>	R2, R0, R1	<b>10110110</b>

## EXCLUSIVE OR

	R0	10100010
	R1	00110100
<b>EOR</b>	R2, R0, R1	<b>10010110</b>

## NOT

	R0	10100010
<b>MVN</b>	R2, R0	<b>01011101</b>

## SHIFT RIGHT

	R0	10100010
	<b>R0&gt;&gt;2</b>	<b>00101000</b>
<b>LSR</b>	R0, #2	

## SHIFT LEFT

	R0	10100010
	<b>R0&lt;&lt;2</b>	<b>10001000</b>
<b>LSL</b>	R0, #2	

- With computers, sometimes bits are used to mask bits. That is, they are utilized to turn bits **ON** or **OFF**.
- Typically, **OR** is used to turn items **ON** (or **set**) a bit and **AND** is utilized to turn items **OFF** (or **clear**) a bit.
- [https://en.wikipedia.org/wiki/Mask\\_\(computing\)](https://en.wikipedia.org/wiki/Mask_(computing))

# Masking



- Masking example:

- Suppose **A** holds an **unknown** binary number.
- You want to turn **ON** all bits in **A**, but you don't want change the unknown value in **bit 3**.
- This operation can be performed by using a bitwise **OR** operation with a **MASK** variable equal to **11110111**.

A → R0	????????
MASK → R1	11110111
<b>ORR R0, R0, R1</b>	
<b>A → R0 = 1111?111</b>	

Bit 3 does not change and it is still unknown.

# Masking



- Masking example:

- Now, using the final **A** value from the previous slide, suppose you want to turn **OFF** bit 3 in **A**.
- This operation can be performed by using a bitwise **NOT** operation, followed by a bitwise **AND** operation, with a **MASK** variable equal to **00001000**.

$A \rightarrow R0$	<b>1111?111</b>
$\text{MASK} \rightarrow R1$	<b>00001000</b>
<b>MVN R1, R1</b> <b>AND R0, R0, R1</b>	
$A \rightarrow R0 =$	<b>11110111</b>

Now, bit 3 is equal to zero.

# Masking – Checking a bit



Uses bitwise **AND**.

<b>a → R0</b>	a <sub>7</sub>	a <sub>6</sub>	<b>a<sub>5</sub></b>	a <sub>4</sub>	a <sub>3</sub>	a <sub>2</sub>	a <sub>1</sub>	a <sub>0</sub>
<b>MASK → R1</b>	0	0	<b>1</b>	0	0	0	0	0
<b>AND R2, R0, R1</b>	0	0	<b>a<sub>5</sub></b>	0	0	0	0	0

# Masking – Setting a bit



Uses bitwise **OR**.

<b>a → R0</b>	a <sub>7</sub>	a <sub>6</sub>	<b>a<sub>5</sub></b>	a <sub>4</sub>	a <sub>3</sub>	a <sub>2</sub>	a <sub>1</sub>	a <sub>0</sub>
<b>MASK → R1</b>	0	0	<b>1</b>	0	0	0	0	0
<b>ORR R0, R0, R1</b>	a <sub>7</sub>	a <sub>6</sub>	<b>1</b>	a <sub>4</sub>	a <sub>3</sub>	a <sub>2</sub>	a <sub>1</sub>	a <sub>0</sub>

# Masking – Clearing a bit



Uses bitwise **NOT**, followed by bitwise **AND**.

<b>a → R0</b>	a <sub>7</sub>	a <sub>6</sub>	<b>a<sub>5</sub></b>	a <sub>4</sub>	a <sub>3</sub>	a <sub>2</sub>	a <sub>1</sub>	a <sub>0</sub>
<b>MASK → R1</b>	0	0	<b>1</b>	0	0	0	0	0
<b>BIC R0, R1</b>	a <sub>7</sub>	a <sub>6</sub>	<b>0</b>	a <sub>4</sub>	a <sub>3</sub>	a <sub>2</sub>	a <sub>1</sub>	a <sub>0</sub>

The **BIC** instruction incorporates the **NOT** and **AND** in a single instruction.

# Masking – Toggling a bit



Uses bitwise **EXCLUSIVE-OR (XOR)**.

<b>a → R0</b>	a <sub>7</sub>	a <sub>6</sub>	<b>a<sub>5</sub></b>	a <sub>4</sub>	a <sub>3</sub>	a <sub>2</sub>	a <sub>1</sub>	a <sub>0</sub>
<b>MASK → R1</b>	0	0	<b>1</b>	0	0	0	0	0
<b>EOR R0, R0, R1</b>	a <sub>7</sub>	a <sub>6</sub>	<b>NOT (a<sub>5</sub>)</b>	a <sub>4</sub>	a <sub>3</sub>	a <sub>2</sub>	a <sub>1</sub>	a <sub>0</sub>

- Go to Canvas and answer all **FIVE** questions in the following assignment: **Lab 2 – Week 2**.
  - **Canvas will automatically grade your work! You don't need to show your work to the T.A.!**
  - The T.A. will help you with any problem you may face while answering the Canvas quiz.
  - All questions should be answered with the help of the **debugging environment** in the STM32CubeIDE.
    - Don't forget to use **Tutorial 4 – Debugging** to help you!
    - Create a project from scratch, a **main.s** file from scratch, and use the concepts you learned today.
    - **We are not interfacing any hardware with the development kit today. So, you don't need to use any include file.**

- **Lab 2 – Pre-lab Quiz is due next class! Pre-lab Quiz is available on Canvas!**
- **Lab 2 – Interfacing the joystick with the LEDs:**
  - **Lab lecture:** Introduction to General Purpose Input and Output (GPIO).